

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



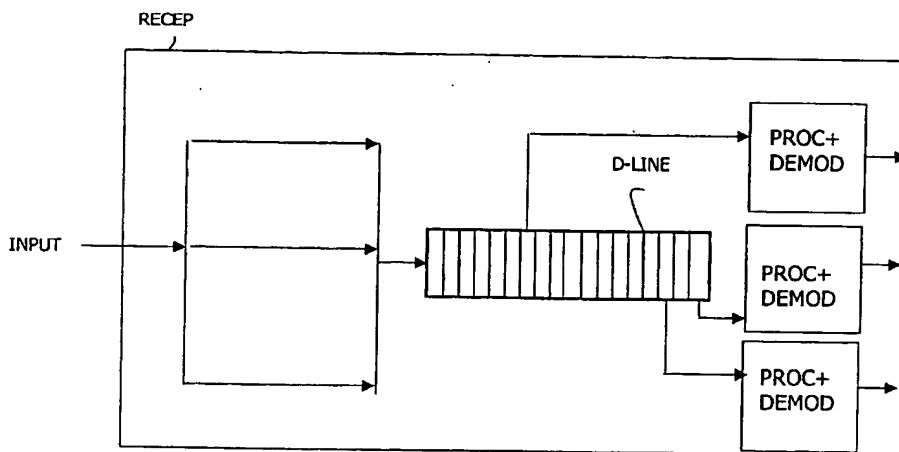
(43) International Publication Date
4 March 2004 (04.03.2004)

PCT

(10) International Publication Number
WO 2004/019510 A1

- (51) International Patent Classification⁷: **H04B 1/707**
- (21) International Application Number:
PCT/IB2003/003399
- (22) International Filing Date: 5 August 2003 (05.08.2003)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
02 10 452 21 August 2002 (21.08.2002) FR
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- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).
- Published:
— with international search report
- For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: DELAY LINE FOR MULTIPLE PROPAGATION PATHS RECEPTION



(57) Abstract: The present invention relates to a delay line (D LINE) for delaying an input signal, said input signal comprising a series of samples (IN TIME, LATE, VOID). The invention is characterized in that it is intended to delay the input signal by a series of delays and that the series is divided into a series of delay sub-lines (ZONE) each intended to write one sample from the series of samples (IN TIME, EARLY, LATE, VOID) of said input signal, and in that it comprises control means (RD DD GEN) intended to generate read addresses of the samples in the delay sub-line (ZONE) from the series of samples (IN TIME, EARLY, LATE, VOID) of the input signal. The read addresses are equal to a difference between a write address of a sample in the delay sub-lines (ZONE) of the input signal and said delays expressed in a number of chip periods.

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INTERNATIONAL SEARCH REPORT

PCT/IB 03/03399

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H04B1/707

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H04B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 1 075 090 A (SAMSUNG ELECTRONICS CO LTD) 7 February 2001 (2001-02-07) figure 3 paragraph '0010! paragraph '0012!	1-5, 7, 10, 12, 13
A	EP 1 162 755 A (LUCENT TECHNOLOGIES INC) 12 December 2001 (2001-12-12) paragraph '0010! figure 3	1, 12, 13

☐ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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P document published prior to the international filing date but later than the priority date claimed

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Y document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

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Date of the actual completion of the international search

11 November 2003

Date of mailing of the international search report

26/11/2003

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INTERNATIONAL SEARCH REPORT

PCT/IB 03/03399

107/10 03/03399

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